

**FERROELECTRIC MEMORY DEVICES WITH EXPANDED PLATE  
LINE AND METHODS OF FABRICATING THE SAME**

**ABSTRACT OF THE DISCLOSURE**

- 5           A ferroelectric memory device includes a lower interlayer dielectric on a semiconductor substrate, a plurality of ferroelectric capacitors, and a plate line. The ferroelectric capacitors are on the lower interlayer dielectric. The plate line extends across and electrically connects to surfaces of at least two adjacent ones of the plurality of ferroelectric capacitors.